

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

High Voltage Circuits Implemented Using Low Voltage
Transistors

Application Number :

Confirmation Number:

First Named Applicant: Sanish JACOB

Attorney Docket Number: TI-37611

Art Unit:

Examiner:

Search string: (4704547).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4704547	1987-11-03	Kirsch			

Signature

Examiner Name	Date